#### CMSC 28100

## Introduction to Complexity Theory

Spring 2024 Instructor: William Hoza



## *k*-CNF formulas

- A *k*-CNF formula is an AND of ORs of literals in which every clause has at most *k* literals
- Example of a 3-CNF formula with two clauses:

$$\phi = (x_1 \lor \bar{x}_2 \lor \bar{x}_6) \land (x_5 \lor x_1 \lor x_2)$$

### The Cook-Levin Theorem

• Define k-SAT = { $\langle \phi \rangle : \phi$  is a satisfiable k-CNF formula}

The Cook-Levin Theorem: 3-SAT is NP-complete

- **Proof:** 3-SAT  $\in$  NP (guess a satisfying assignment)
- To show that 3-SAT is NP-hard, we will reduce from CIRCUIT-SAT

### Gate gadgets

• Define the following Boolean functions:

CHECK-NOT
$$(g, y) = \begin{cases} 1 & \text{if } g = \overline{y} \\ 0 & \text{otherwise} \end{cases}$$
  
CHECK-AND $(g, y, z) = \begin{cases} 1 & \text{if } g = (y \land z) \\ 0 & \text{otherwise} \end{cases}$   
CHECK-OR $(g, y, z) = \begin{cases} 1 & \text{if } g = (y \lor z) \\ 0 & \text{otherwise} \end{cases}$ 

• Each can be represented by a 3-CNF formula. (Every function has a CNF representation!)

### Reduction from CIRCUIT-SAT to 3-SAT

- Reduction:  $f(\langle C \rangle) = \langle \phi \rangle$ , where  $\phi$  is a 3-CNF defined as follows
- Circuit C has variables  $x_1, x_2, \dots, x_n$  and AND/OR/NOT gates  $g_1, \dots, g_m$
- Assume without loss of generality that  $g_m$  is the output gate
- Formula  $\phi$  has n + m variables, which we denote  $x_1, \dots, x_n, g_1, \dots, g_m$
- Note: In C, " $g_i$ " is the name of a gate. In  $\phi$ , " $g_i$ " is the name of a variable

### Reduction from CIRCUIT-SAT to 3-SAT

• For each AND/OR/NOT gate  $g_i$  in the circuit C, define a 3-CNF  $\phi_i$ :



• Reduction produces  $\phi := \phi_1 \land \phi_2 \land \cdots \land \phi_m \land (g_m)$ 

#### Reduction example

 $g_5$ 

 $g_3$ 

 $x_1$ 

 $g_1$ 

 $g_4$ 

 $x_2$ 

 $g_2$ 

• 
$$\phi_1 = \text{CHECK-NOT}(g_1, x_1) = (g_1 \lor x_1) \land (\overline{g}_1 \lor \overline{x}_1)$$

• 
$$\phi_2 = \text{CHECK-NOT}(g_2, x_2) = (g_2 \lor x_2) \land (\overline{g}_2 \lor \overline{x}_2)$$

•  $\phi_3 = \text{CHECK-AND}(g_3, x_1, g_2) = (\bar{g}_3 \lor x_1) \land (\bar{g}_3 \lor g_2) \land (g_3 \lor \bar{x}_1 \lor \bar{g}_2)$ 

• 
$$\phi_4 = \text{CHECK-AND}(g_4, g_1, x_2) = (\bar{g}_4 \lor g_1) \land (\bar{g}_4 \lor x_2) \land (g_4 \lor \bar{g}_1 \lor \bar{x}_2)$$

•  $\phi_5 = \text{CHECK-OR}(g_5, g_3, g_4) = (g_5 \lor \bar{g}_3) \land (g_5 \lor \bar{g}_4) \land (\bar{g}_5 \lor g_3 \lor g_4)$ 

$$\phi = (g_1 \lor x_1) \land (\bar{g}_1 \lor \bar{x}_1) \land (g_2 \lor x_2) \land (\bar{g}_2 \lor \bar{x}_2) \land (\bar{g}_3 \lor x_1) \land (\bar{g}_3 \lor g_2) \\ \land (g_3 \lor \bar{x}_1 \lor \bar{g}_2) \land (\bar{g}_4 \lor g_1) \land (\bar{g}_4 \lor x_2) \land (g_4 \lor \bar{g}_1 \lor \bar{x}_2) \land (g_5 \lor \bar{g}_3) \\ \land (g_5 \lor \bar{g}_4) \land (\bar{g}_5 \lor g_3 \lor g_4) \land (g_5)$$

### YES maps to YES

- Claim: If the circuit C is satisfiable, then the 3-CNF formula  $\phi$  is also satisfiable
- **Proof:** We are assuming there is some  $x \in \{0, 1\}^n$  such that C(x) = 1
- For each i, assign to  $g_i$  (the variable) the value that  $g_i$  (the gate) outputs when we evaluate C on x
- We claim that  $\phi(x_1, ..., x_n, g_1, ..., g_m) = 1$ . Indeed, each  $\phi_i$  is satisfied because of the circuit structure, and  $g_m = 1$  because C(x) = 1

#### NO maps to NO

- Claim: If C is not satisfiable, then  $\phi$  is not satisfiable
- **Proof sketch:** We will prove the contrapositive: if  $\phi$  is satisfiable, then *C* is satisfiable

• If 
$$\phi(x_1, ..., x_n, g_1, ..., g_m) = 1$$
, then we claim  $C(x_1, ..., x_n) = 1$ 

• Indeed, by induction on the circuit structure,  $g_i$  (the variable) must be equal to the value that  $g_i$  (the gate) outputs when we evaluate C on x. Furthermore,  $g_m = 1$ 

### Reduction efficiency

- Reduction is computable in polynomial time
- For each gate in the circuit, we write down O(1) clauses, and it is

straightforward to compute what they are





## Chaining reductions together

3-SAT is the starting point for many NP-hardness proofs



• We are finally ready to use the hardness of 3-SAT to prove that CLIQUE is NP-complete

## CLIQUE is NP-complete

• Recall CLIQUE = { $\langle G, k \rangle$  : G contains a k-clique}

**Theorem:** CLIQUE is NP-complete

- **Proof:** We showed CLIQUE ∈ NP in a previous class
- To prove that CLIQUE is NP-hard, we will do a reduction from 3-SAT

## Reduction from 3-SAT to CLIQUE

- Let  $\phi$  be a 3-CNF formula (an instance of 3-SAT)
- Reduction:  $f(\langle \phi \rangle) = \langle G, k \rangle$ 
  - k is the number of clauses in  $\phi$
  - G is a graph on  $\leq 3k$  vertices defined as follows

#### Reduction from 3-SAT to CLIQUE

For each clause (ℓ<sub>1</sub> ∨ ℓ<sub>2</sub> ∨ ℓ<sub>3</sub>), create a
"group" of three vertices labeled

 $\ell_1,\ell_2,\ell_3$ 

- (If the clause only has one or two literals, then only use one or two vertices)
- Put an edge {u, v} if u and v are in different groups and u and v do not have contradictory labels (x<sub>i</sub> and x̄<sub>i</sub>)

• E.g.,  $\phi = (x_1 \lor x_2 \lor \overline{x}_5) \land (\overline{x}_1 \lor x_4 \lor x_6)$  $\land (x_2 \lor x_4 \lor \overline{x}_3) \land (x_3 \lor \overline{x}_6 \lor x_1)$ 



### YES maps to YES

- Suppose  $\phi$  is satisfiable, i.e., there exists a satisfying assignment x
- In each clause, at least one literal is satisfied by x
- Therefore, in each group, at least one vertex is "satisfied by x," i.e., it is labeled by a literal that is satisfied by x
- Let S be a set consisting of one satisfied vertex from each group
- Then S is a k-clique (vertices in S cannot have contradictory labels)

#### NO maps to NO

- Suppose *G* has a *k*-clique *S*
- Let x be an assignment that satisfies each vertex in S (this exists because no two vertices in S have contradictory labels)
- S cannot contain two vertices from a single group, and |S| = k, so S must contain one vertex from each group
- Therefore, x satisfies at least one literal in each clause, i.e., x satisfies  $\phi$

### Poly-time computable

• Hopefully it is clear that the reduction  $f(\langle \phi \rangle) = \langle G, k \rangle$  can be computed in polynomial time



## NP-completeness is everywhere

- There are thousands of known NP-complete problems!
- These problems come from many different areas of study
  - Logic, graph theory, number theory, scheduling, optimization, economics, physics, chemistry, biology, ...

# Proving that L is NP-complete ("cheat sheet")

- 1. Prove that  $L \in NP$ 
  - What is the certificate? How can you verify a purported certificate in polynomial time?
- 2. Prove that *L* is NP-hard
  - Which NP-complete language  $L_{\text{HARD}}$  are you reducing from?
  - What is the reduction? Is it polynomial-time computable?
  - YES maps to YES: Assume there is a certificate x showing  $w \in L_{HARD}$ . In terms of x, construct a certificate y showing that  $f(w) \in L$ .
  - NO maps to NO: (Contrapositive) Assume there is a certificate y showing  $f(w) \in L$ . In terms of y, construct a certificate x showing that  $w \in L_{HARD}$ .

## NP-complete languages stand or fall together

- If you design a poly-time algorithm for one NP-complete language, then
  - P = NP, so all NP-complete languages can be decided in poly time!

 If you prove that one NP-complete language cannot be decided in poly time, then P ≠ NP, so no NP-complete language can be decided in poly time!

## Final exam cutoff point

- Final exam will be Wednesday, May 22 from 10am to noon in this room (STU 105)
- The exam is cumulative
- To prepare for the final exam, you only need to study the material up to this point (including problem set 7)